

## **Abstract**

This project paper explores the application of Work Study methods to improve the manufacturing processes in the current semiconductor manufacturing environment. The scope of the project is narrowed down to the Quality Assurance Out-going Final Inspection (QA\ OFI) Gate process step at the End-Of-Line (EOL) Operations in Motorola Malaysia Sendirian Berhad. This QA OFI Gate process step had recently become a bottleneck to the total EOL operations and in need of productivity improvement. It was observed that Work Study methods were not being used extensively or applied in a systematic manner in the company being studied. Many changes were made but usually in a less systematic manner and in most instances it is difficult to ascertain if the alterations in procedure is really effective. The findings of this project will be used to determine if this Work Study methodology can be practically applied in an actual manufacturing environment and then decide if other processes can be improved using a similar methodology.